

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	0	(memor\$3 near phase or stor\$3 near3 phase\$1 or register\$3 near3 phase or RAM near3 phase\$1 or ROM near3 phase or LUT near2 phase\$1 or record\$3 near2 phase\$1) with (pilot\$1) with (second adj2 device\$1 or second adj2 circuit\$3 or second adj2 network\$3 or second adj2 interface\$1 or second adj2 modem\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:20
2	BRS	L2	4	(memor\$3 near phase or stor\$3 near3 phase\$1 or register\$3 near3 phase or RAM near3 phase\$1 or ROM near3 phase or LUT near2 phase\$1 or record\$3 near2 phase\$1) same (pilot\$1) same (second adj2 device\$1 or second adj2 circuit\$3 or second adj2 network\$3 or second adj2 interface\$1 or second adj2 modem\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:12
3	BRS	L3	487	(memor\$3 or stor\$3 or register\$3 or RAM\$1 or ROM or LUT or record\$3 or buffer) same (pilot\$1) same (second adj2 device\$1 or second adj2 circuit\$3 or second adj2 network\$3 or second adj2 interface\$1 or second adj2 modem\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:22

	Comments	Error Definition	Errors
1			
2			
3			

	Type	L #	Hits	Search Text	DBs	Time Stamp
4	BRS	L4	11	(memor\$3 near3 phase\$1 or stor\$3 near3 phase\$1 or register\$3 near3 phase\$1 or RAM\$1 near3 phase\$1 or ROM near3 phase\$1 or LUT near3 phase\$1 or record\$3 near3 phase\$1 or buffer near3 phase\$1) same (first adj2 pilot\$1)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:23
5	BRS	L5	0	3 and 4	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:20
6	BRS	L6	160	(first adj2 pilot\$1) with (second adj2 device\$1 or second adj2 circuit\$3 or second adj2 network\$3 or second adj2 interface\$1 or second adj2 modem\$1 or second adj2 terminal\$1)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:26

	Comments	Error Definition	Errors
4			
5			
6			

	Type	L #	Hits	Search Text	DBs	Time Stamp
7	BRS	L7	14	(memor\$3 or stor\$3 or register\$3 or RAM\$1 or ROM or LUT or record\$3 or buffer) same 6	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:22
8	BRS	L8	5	(memor\$3 near3 phase\$1 or stor\$3 near3 phase\$1 or register\$3 near3 phase\$1 or RAM\$1 near3 phase\$1 or ROM near3 phase\$1 or LUT near3 phase\$1 or record\$3 near3 phase\$1 or buffer\$3 near3 phase\$1) and 6	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:31
9	BRS	L10	11	(memor\$3 near3 phase\$1 or stor\$3 near3 phase\$1 or register\$3 near3 phase\$1 or RAM\$1 near3 phase\$1 or ROM near3 phase\$1 or LUT near3 phase\$1 or record\$3 near3 phase\$1 or buffer\$3 near3 phase\$1) same(first adj2 pilot\$1)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:28

	Comments	Error Definition	Errors
7			
8			
9			

	Type	L #	Hits	Search Text	DBs	Time Stamp
10	BRS	L11	0	9 and 10	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:28
11	BRS	L9	25	(first adj2 pilot\$1) adj4 (second adj2 device\$1 or second adj2 circuit\$3 or second adj2 network\$3 or second adj2 interface\$1 or second adj2 modem\$1 or second adj2 terminal\$1)	US- PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWEN T; IBM_TD B	2007/08/29 12:28
12	BRS	L12	12	((MAGED) near2 (BARSOUM)).INV.	US- PGPUB; USPAT; USOCR	2007/08/29 12:29
13	BRS	L13	0	6 and 12	US- PGPUB; USPAT; USOCR	2007/08/29 12:30
14	BRS	L14	0	9 and 12	US- PGPUB; USPAT; USOCR	2007/08/29 12:30

	Comments	Error Definition	Errors
10			
11			
12			
13			
14			

	Type	L #	Hits	Search Text	DBs	Time Stamp
15	BRS	L15	0	3 and 12	US- PGPUB; USPAT; USOCR	2007/08/29 12:30

	Comments	Error Definition	Errors
15			